

FIG. 1

V_{bb} Substrate Bias (Volts)

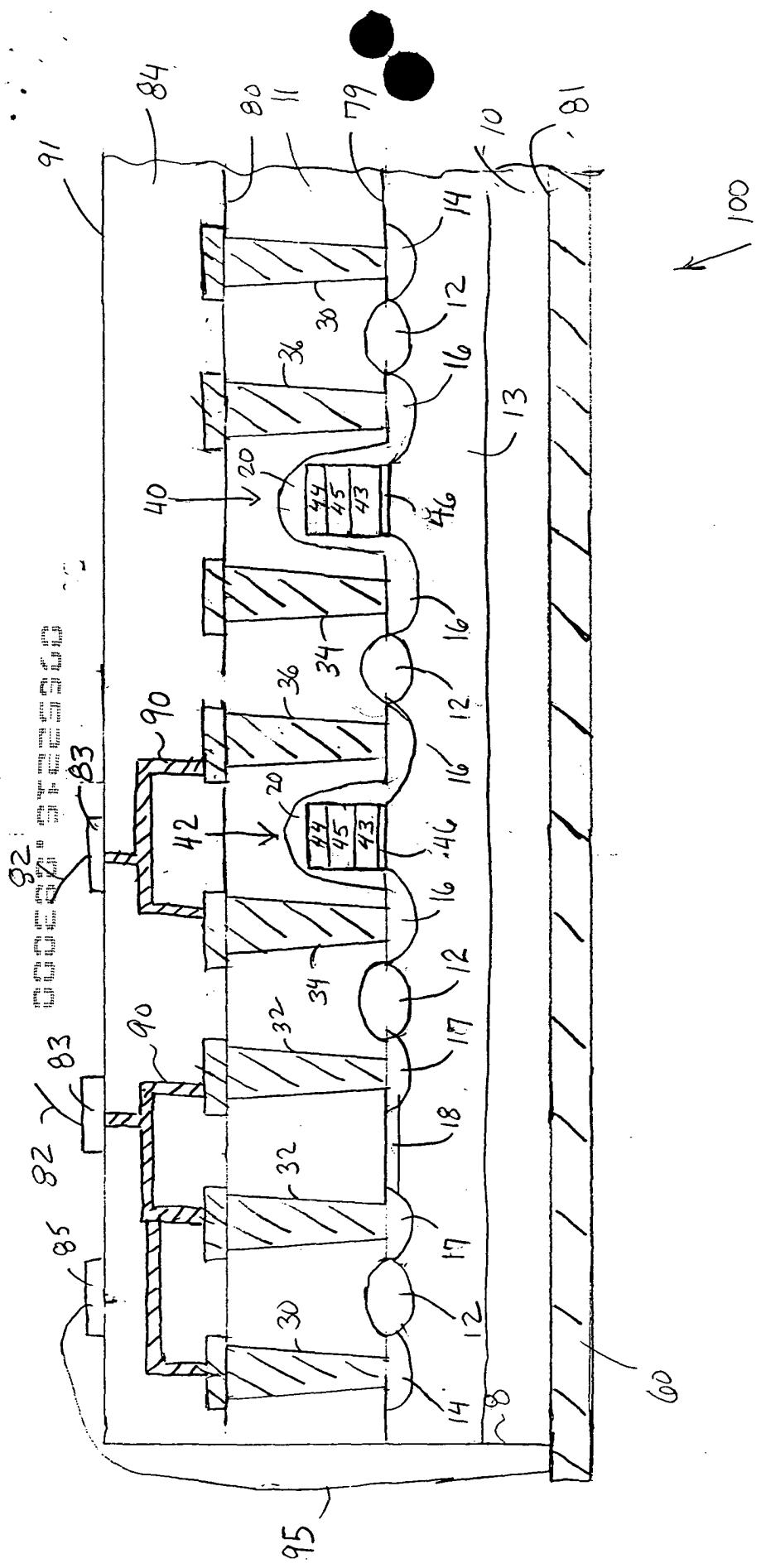
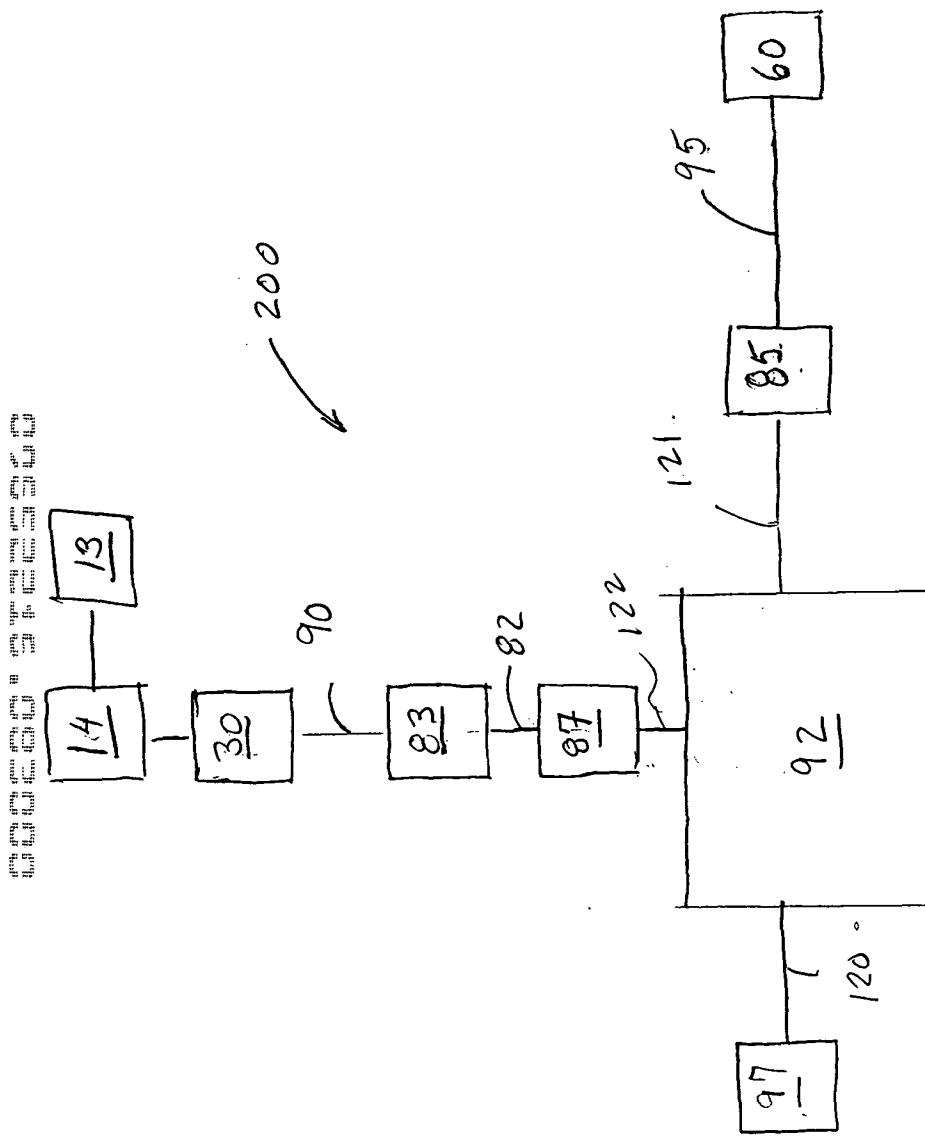


FIG. 2

FIG. 3



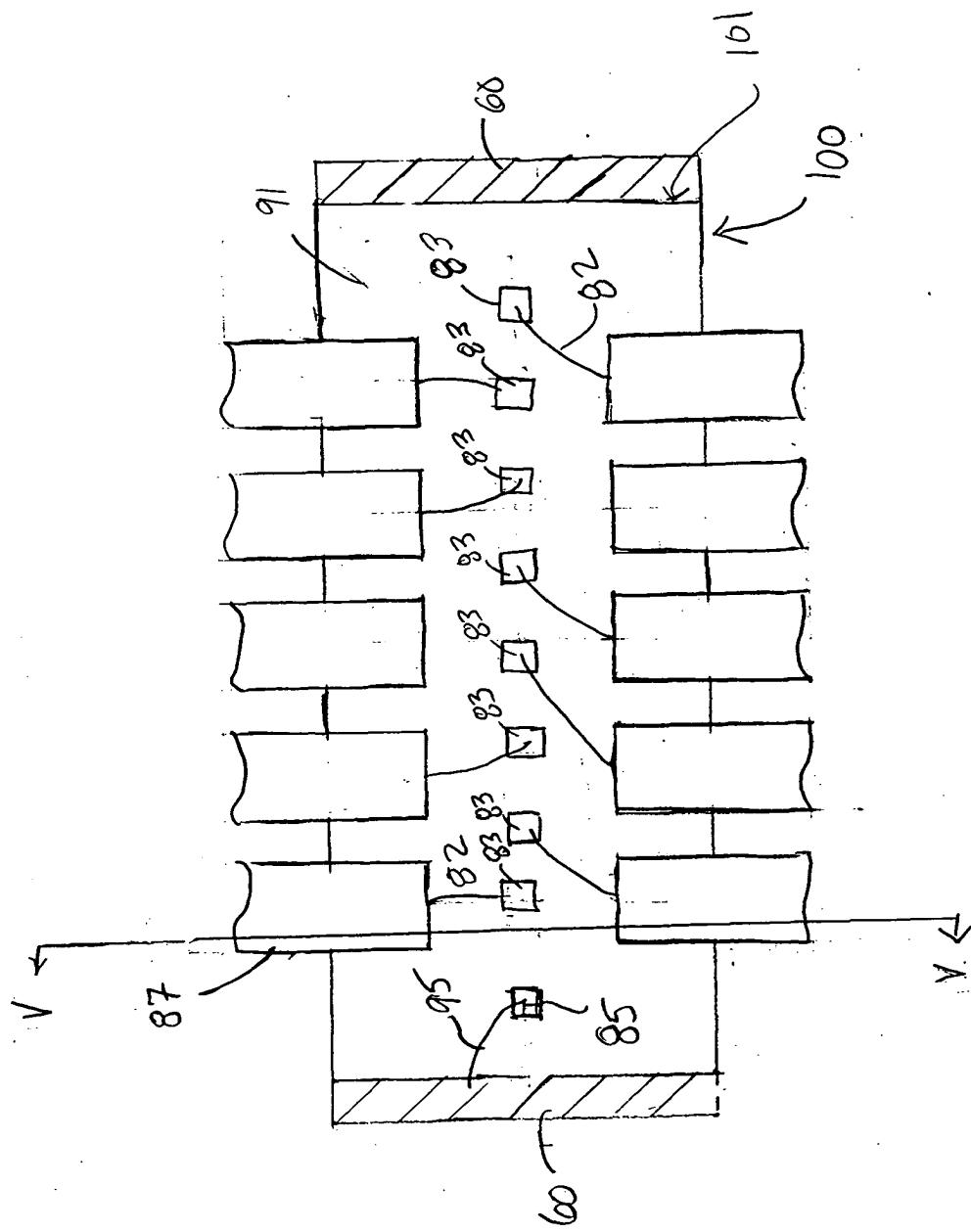
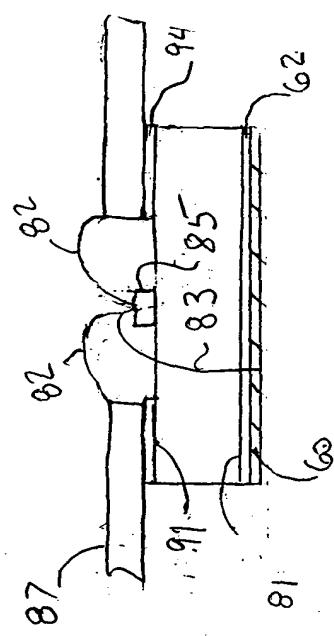


FIG. 4

FIG. 5



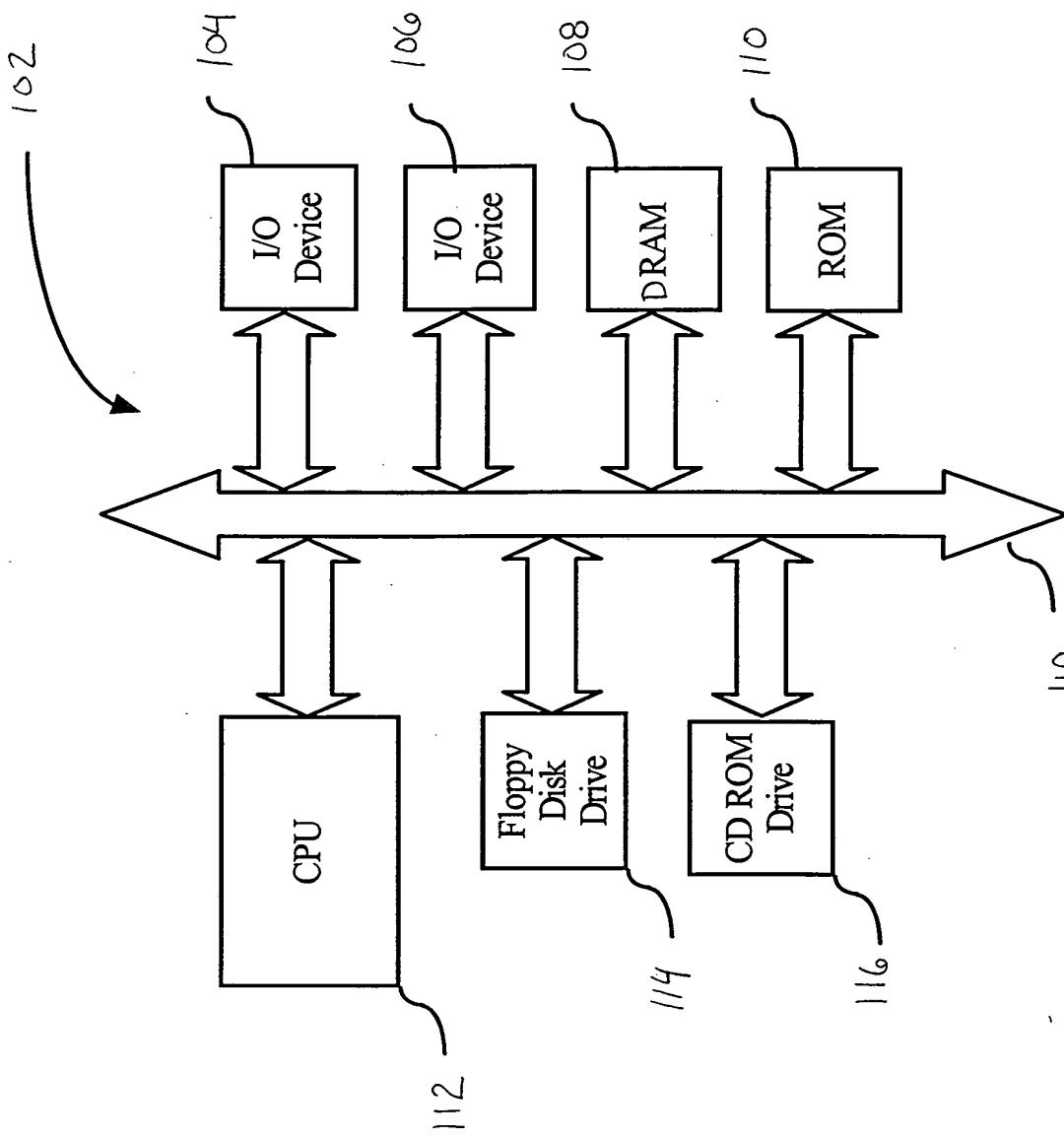


Fig. 6